



# Hands-on Training in Silicon Photonic Technology for NextGen Systems

(Exclusive Opportunity for PG Level Students/ Research Scholars/ Early Career Professionals)



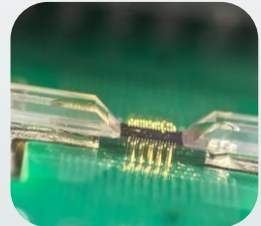
5<sup>th</sup> – 7<sup>th</sup> September, 2025



CoE-CPPICS  
Dept. of Electrical Engineering  
IIT Madras, Chennai - 600036

## What you will learn!

- Programmable Silicon Photonics: Technology and real-world impact
- Fundamentals of photonic devices and circuits
- Hands-on design & simulation of photonic components
- Fabrication insights from a foundry perspective
- Device characterization & real-time experiments
- Applications: Data Comm, 5G/6G, quantum computing and communication



## Grand Challenges

Grand challenges will be announced at the end of the workshop. The best design will be selected for chip fabrication through CPPICS.

**PG-level Students/Research Scholars: 10,000 INR**

**Early Career Professionals/Industry: 25,000 INR**

(Accommodation must be taken care by individuals)

Full Schedule:

[Click Here](#)

Limited Seats

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**Application Deadline: 15<sup>th</sup> August 2025**

**Application Confirmation to Shortlisted Candidates : 20<sup>th</sup> August 2025**